

Electronic Acknowledgement Receipt

EFS ID:	1471154
Application Number:	10776701
International Application Number:	
Confirmation Number:	3435
Title of Invention:	Impedance-matched write circuit with shunted matching resistor
First Named Inventor/Applicant Name:	Hao Fang
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Filer:	Kevin M. Mason
Filer Authorized By:	
Attorney Docket Number:	2-4
Receipt Date:	29-JAN-2007
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Application Type:	Utility

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		1150-1246_AMD.pdf	131451	yes	3

Multipart Description/PDF files in .zip description				
Document Description		Start	End	
Amendment - After Non-Final Rejection		1	1	
Applicant Arguments/Remarks Made in an Amendment		2	3	

Warnings:

Information:

Total Files Size (in bytes):	131451
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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